

FIG. 1

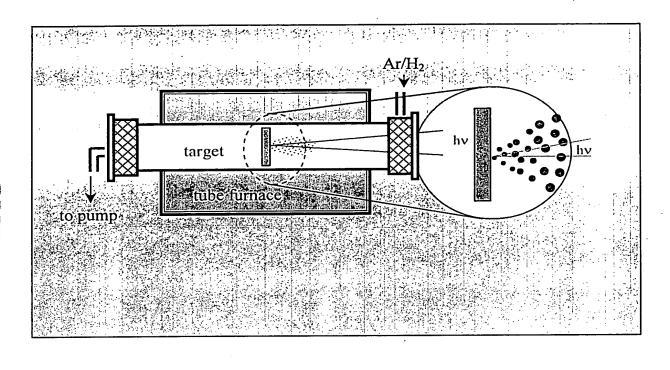


FIG. 2

FIG. 3

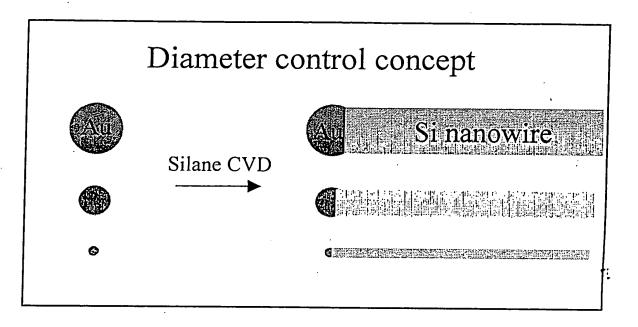
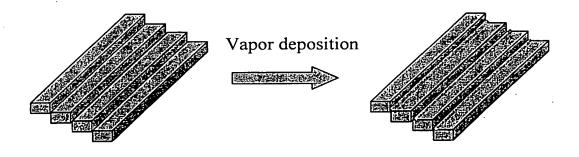
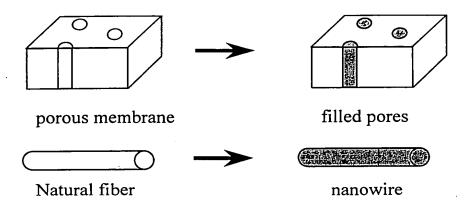


FIG. 4

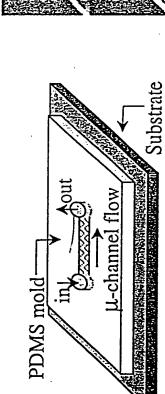


**FIG.** 5



**FIG.** 6

## Orthogonal Assembly of Semiconductor Nanowires



IG. 7A

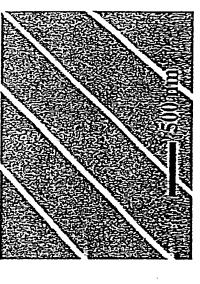


FIG. 7B

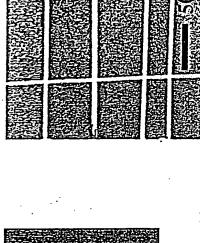
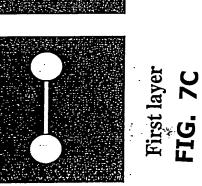


FIG 7



Second layer FIG. 7D



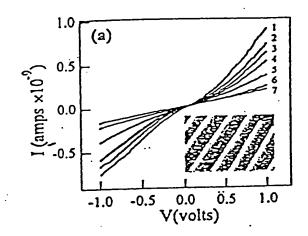


FIG. 8A

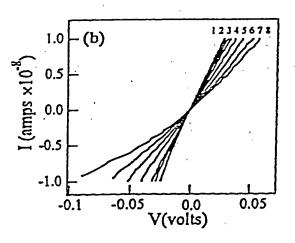


FIG. 8B

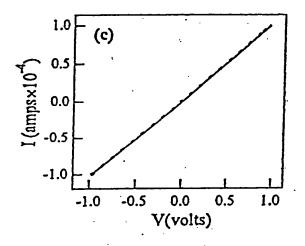


FIG. 8C

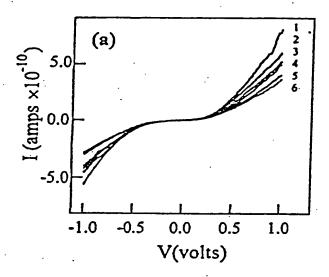


FIG. 9A

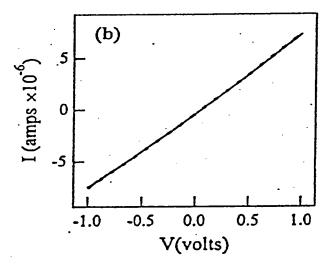


FIG. 9B

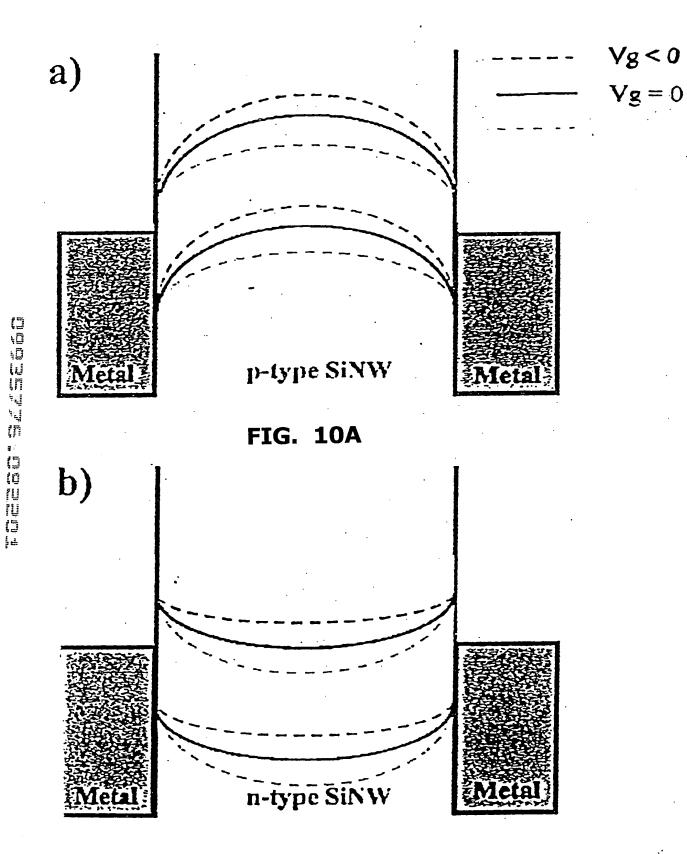


FIG. 10B

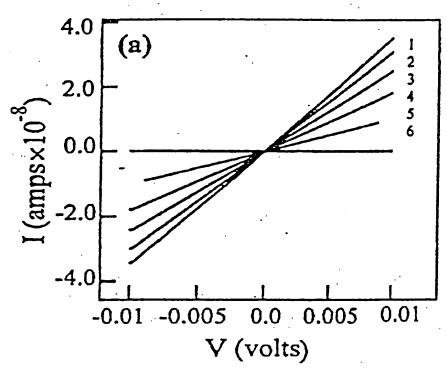


FIG. 11A

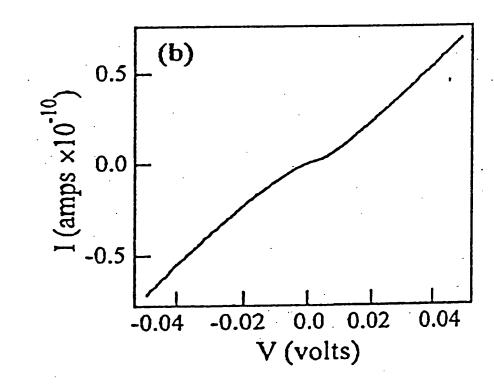


FIG. 11B

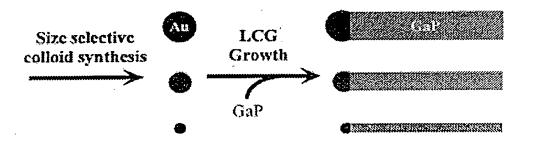
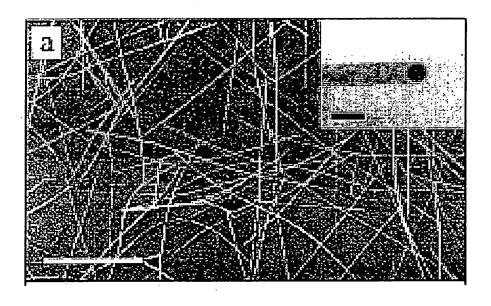
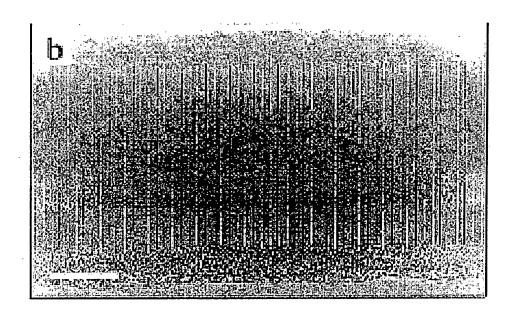


FIG. 12

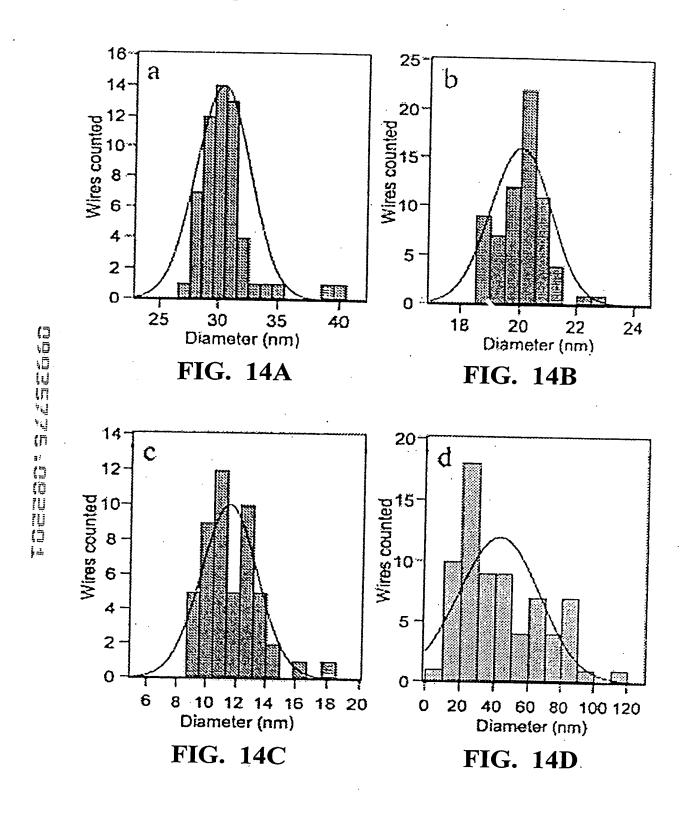


**FIG. 13A** 



**FIG. 13B** 

,



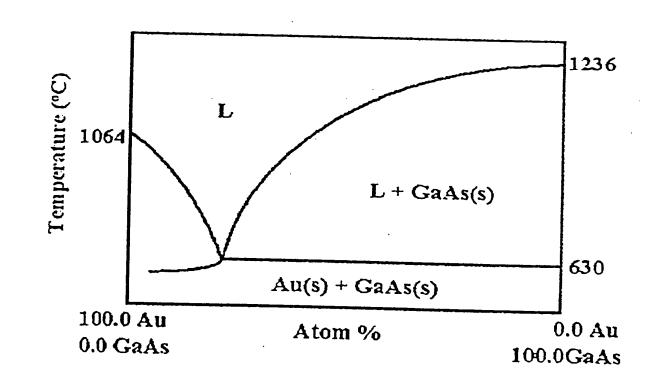
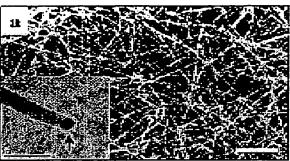


FIG. 15





**FIG. 16A** 

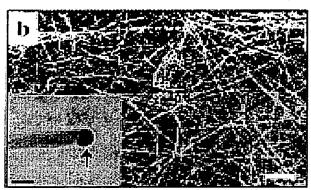
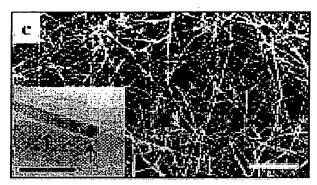


FIG. 16B



**FIG. 16C** 

## Best Available Copy

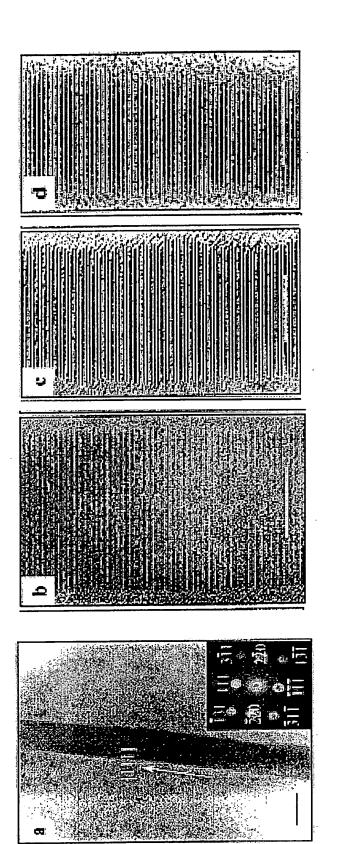
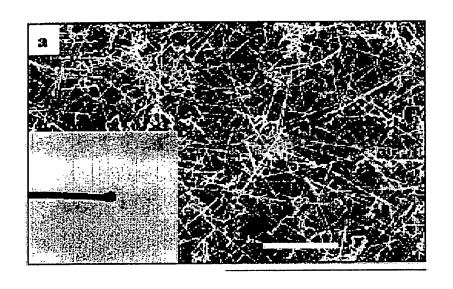


FIG. 17C FIG. 17B **FIG. 17A** 



**FIG. 18A** 

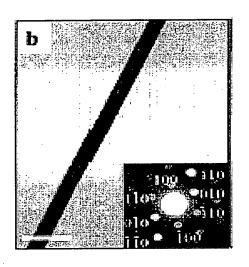
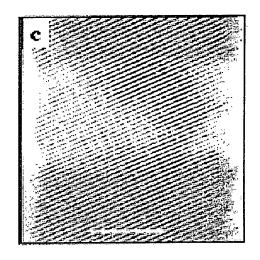


FIG. 18B



**FIG. 18C** 

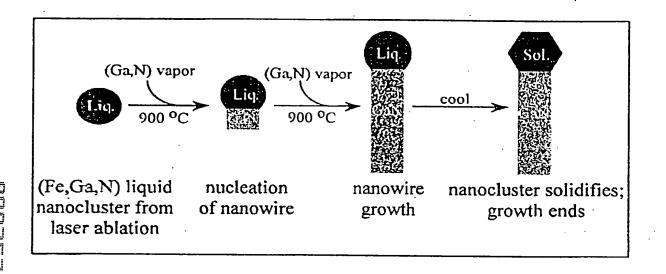
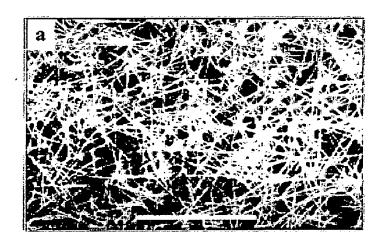
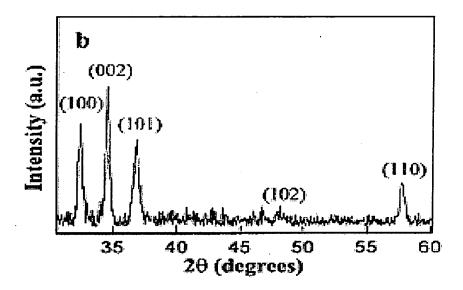


FIG. 19

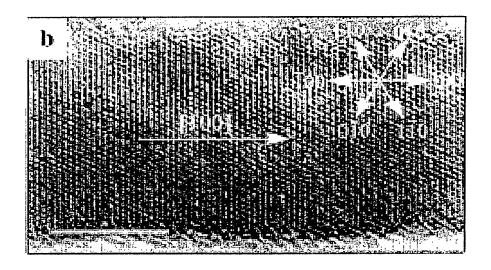


**FIG. 20A** 



**FIG. 20B** 

**FIG. 21A** 



**FIG. 21B** 

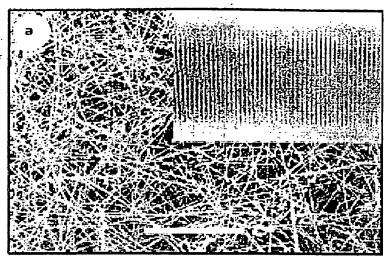


FIG. 22A

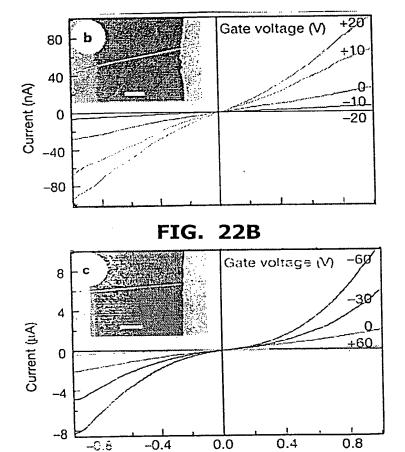


FIG. 22C

Voltage (V)

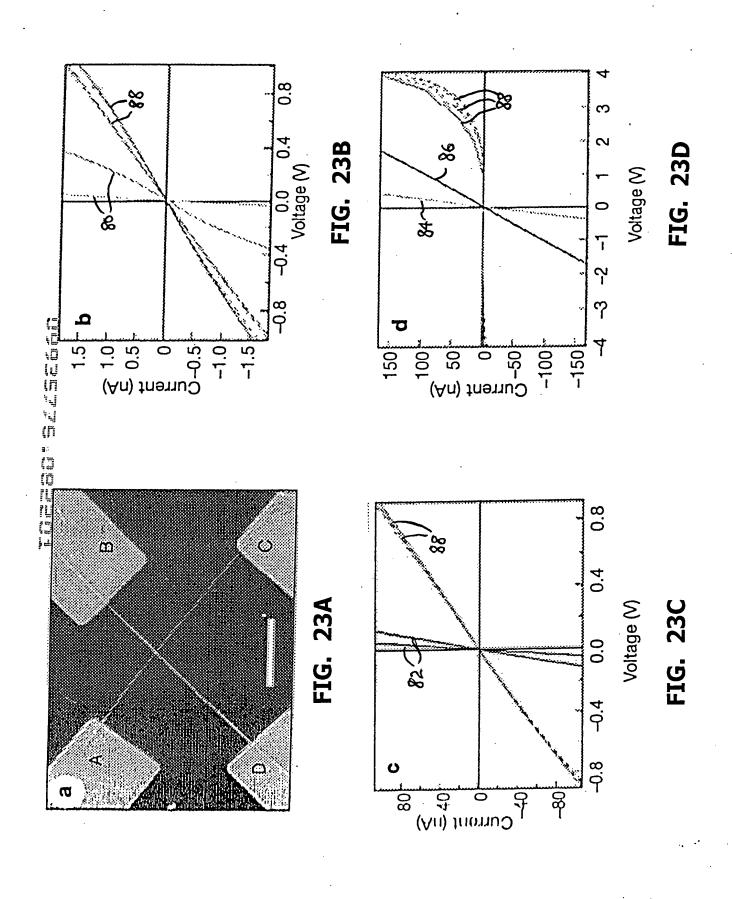




FIG. 24A

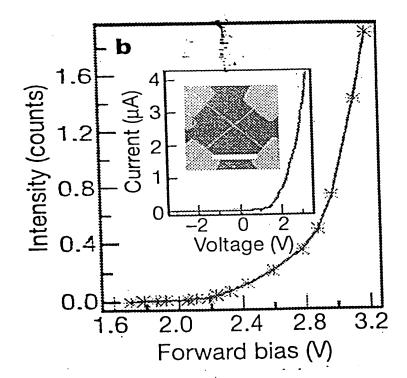


FIG. 24B

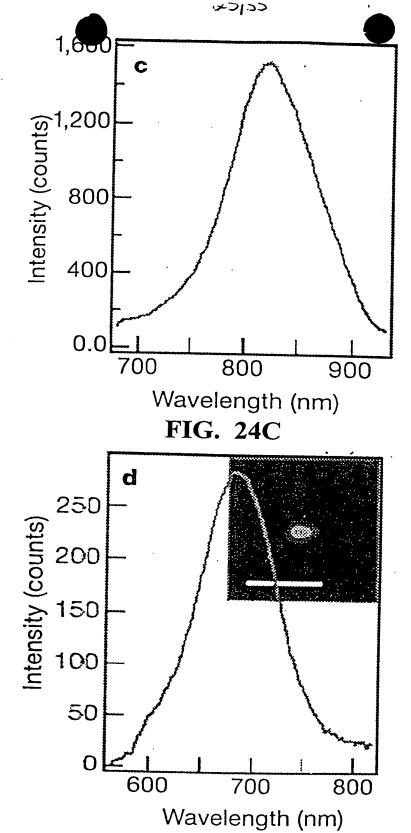
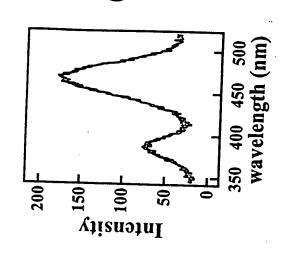
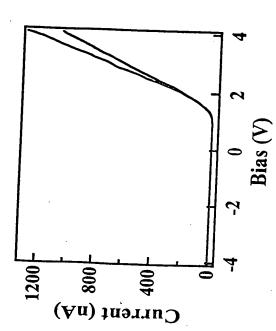


FIG. 24D

FIG. 25C







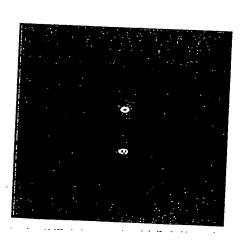
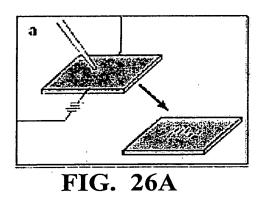
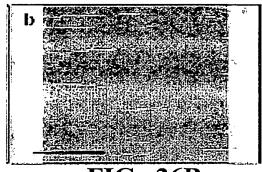


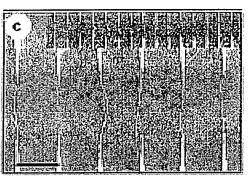
FIG. 25A

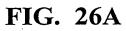
FIG. 25B

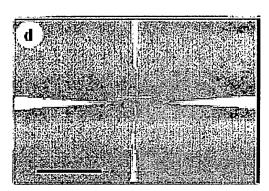




**FIG. 26B** 







**FIG. 26B** 

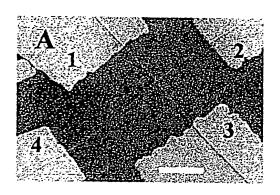
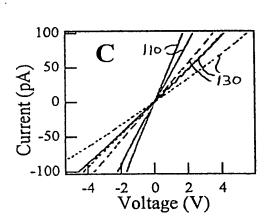


FIG. 27A



والمراقب المساوية المساورة المالية المساورة المساورة المساورة المساورة المساورة المساورة المساورة المساورة الم

FIG. 27C

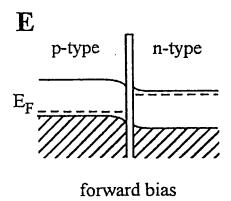


FIG. 27E

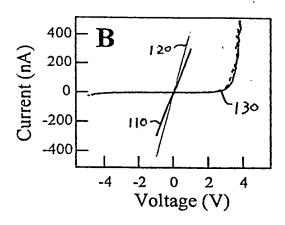


FIG. 27B

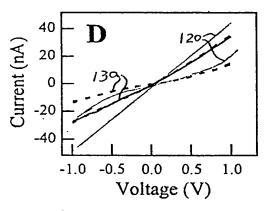
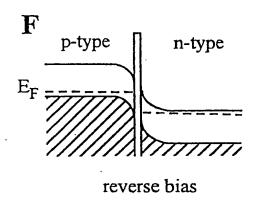
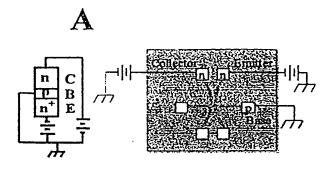


FIG. 27D



**FIG. 27F** 



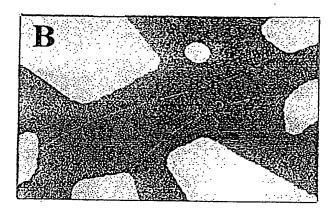
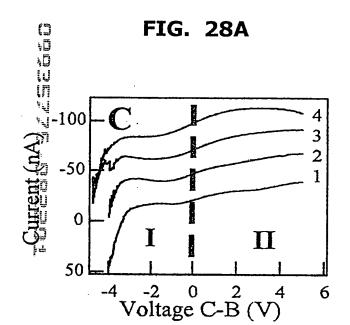


FIG. 28B



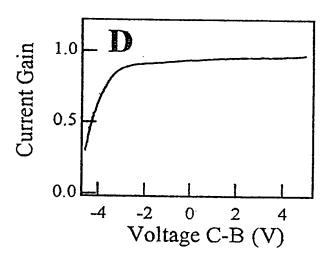


FIG. 28C

FIG. 28D

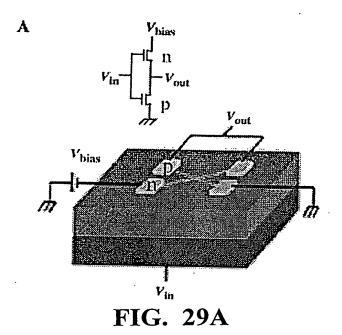


FIG. 29B

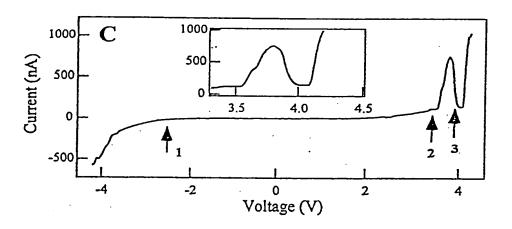


FIG. 29C

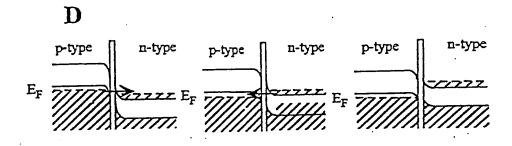
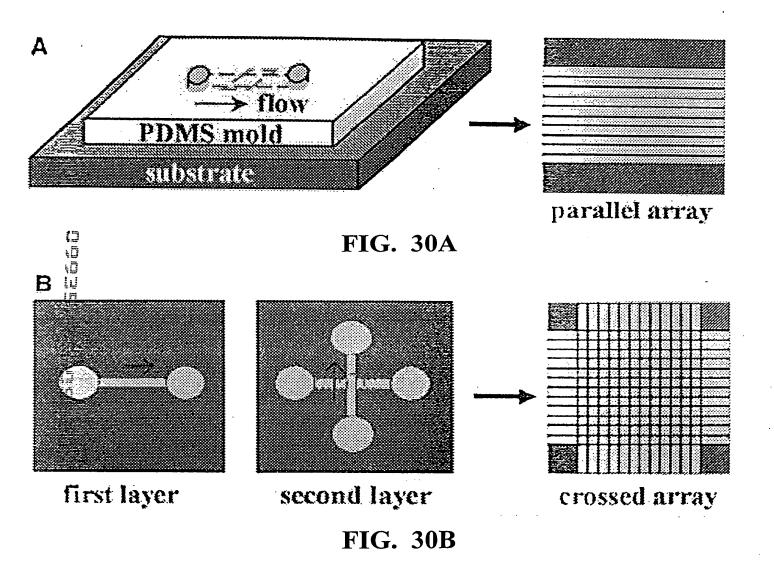
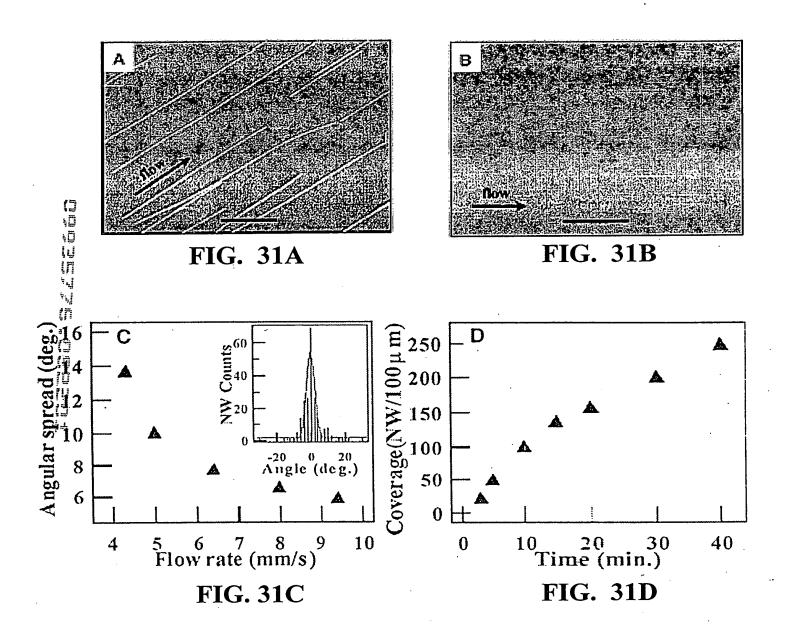
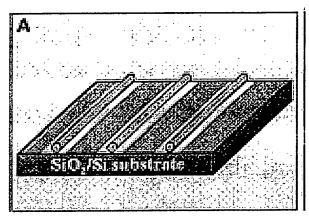


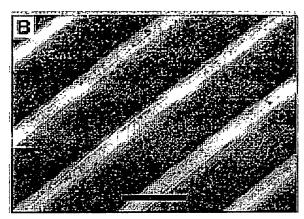
FIG. 29D



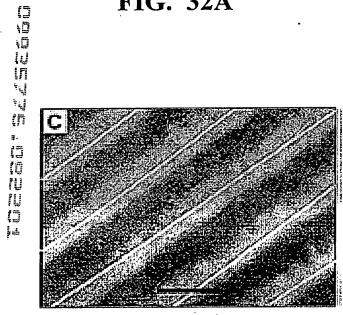




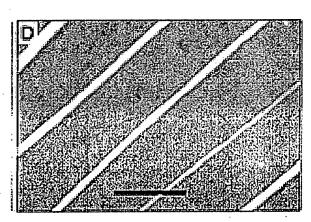
**FIG. 32A** 



**FIG. 32B** 



**FIG. 32C** 



**FIG. 32D** 

